

DEPARTMENT OF ELECTRONICS COMMUNICATION AND INSTRUMENTATION ENGINEERING
 No. 84/Backlog Timetable/B.Tech. (ECI)/ECIE/KITS/2025 Date: 27.10.2025

Submitted to the Principal:

Sub: Conduct of Remedial Classes for Students with Backlogs-Reg.
 Ref.: No. KITS/Acad/Circular/2025/519 dated: 17.10.2025

The following is the timetable for the Remedial classes arranged for students with backlogs under the URR18 R22 Regulations.

Schedule for Remedial Classes

Venue: B - VI 206

S. No	SEMESTER	COURSE NAME	Department	No of Backlog Students	Date	Time	Faculty handling
1.	III	Electronic Measurements and Sensors	ECI	14	28.10. 2025	4:00 pm - 4:45 pm	Dr. O. Anjaneyulu
2.	III	Engineering Mathematics - III	ECI	39	28.10.2025	4:45 pm - 5:30 pm	Dr. V. Anand
3.	III	Digital Circuits & Logic Design	ECI	47	29.10. 2025	4:00 pm - 4:45 pm	Prof. K. Sivani
4.	III	Signals Systems and Random Processes	ECI	37	29.10. 2025	4:45 pm - 5:30 pm	Sri B. Shashikanth
5.	III	Electronic Devices and Applications	ECI	44	30.10. 2025	4:45 pm - 5:30 pm	Prof. K.Venu Madhav
6.	IV	Electromagnetic Theory and Transmission Lines	ECI	12	30.10. 2025	4:45 pm - 5:30 pm	Dr. O. Anjaneyulu
7.	IV	Analog Electronic Circuits	ECI	42	31.10. 2025	4:00 pm - 4:45 pm	Prof. M. Sreelatha
8.	III	Digital Signal Processing	ECI	27	31.10. 2025	4:45 pm - 5:30 pm	Dr. R. Nirmala Devi
9.	VI	Advanced Data Structures	IT	4	01.11.25	10:00 am - 10:45 am & 10:45 am - 11:30 am	Sri N. Srinivas
10.	V	Linear Control Systems	EEE	18	01.11.25	12:00 Noon to 12:45 pm	Sri T. Praveen
11.	V	Linear Integrated Circuits and Applications	ECI	12	03.11.2025	4:00 pm - 4:45 pm	Prof. K. Sivani
12.	V	Analog and Digital Communications	ECI	10	03.11.2025	4:45 pm - 5:30 pm	Smt. E. Sushmitha
13.	V	Internet Of Things	ECI	5	04.11.2025	4:00 pm - 4:45 pm	Dr. M. Raghuram
14.	V	Microcontrollers and Embedded Systems	ECI	8	04.11.2025	4:45 pm - 5:30 pm	Dr. K. Srinivas

Srinivas
 27/10/25

15.	VI	Biomedical Instrumentation	ECI	6	06.11.25	4:00 pm - 4:45 pm	Dr. M. Raghuram
16.	VI	Artificial Intelligence and Machine Learning	ECI	5	06.11.25	4:45 pm - 5:30 pm	Sri B. Venu Maheshwar
17.	VI	Embedded Systems With Arm Processor	ECI	9	07.11.25	4:00 pm - 4:45 pm	Prof. K. Venu Madhav
18.	III	Object Oriented Programming	CSE	21	07.11.25	4:45 pm - 5:30 pm	Dr. N. C. Santosh Kumar
19.	IV	Applicable Mathematics	MHS	29	08.11.2025	11.30am - 12.15pm	Dr. V. Anand
20.	IV	Microprocessor Systems and Interfacing	ECI	32	08.11.2025	12.15pm-01.00pm	Dr. B. Jeevan

Sreenivasa
27/10/25

Prof. K. Sivani
Head, Dept. of ECIE

Copy to:

1. The Dean, AA
2. ECIED Stack file (Backlog Timetable)